## Optoelectronic Materials and Devices Prof. Dr. Deepak Gupta Department of Material Science and Engineering Indian Institute of Technology, Kanpur

## Module - 01 Lecture - 16 Equilibrium Carrier Statistics in Semiconductors: Complete Ionization of Do pant Levels

Welcome to lecture number 16, we will continue from where we left, so let us see where we left.

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$$\frac{N_{D}}{N_{D}} = \exp\left[\frac{\varepsilon_{D} - \varepsilon_{F}}{k_{B}T}\right] \qquad N_{D} = N_{D}^{o} + N_{D}^{+}$$

$$\frac{N_{D}^{+}}{N_{D}^{o}} = \exp\left[\frac{\varepsilon_{D} - \varepsilon_{F}}{k_{B}T}\right] \qquad N_{D} = N_{D}^{o} + N_{D}^{+}$$

$$\frac{N_{D}^{+}}{N_{D} - N_{D}^{+}} = \exp\left[\frac{\varepsilon_{D} - \varepsilon_{F}}{k_{B}T}\right] \qquad N_{D} = N_{D}^{o} + N_{D}^{+}$$

$$\frac{N_{D} - N_{D}^{+}}{N_{D} - N_{D}^{+}} = \exp\left[\frac{\varepsilon_{F} - \varepsilon_{D}}{k_{B}T}\right] \qquad N_{D}^{+} = \frac{1}{1 + \exp\left[\frac{\varepsilon_{F} - \varepsilon_{D}}{k_{B}T}\right]}$$

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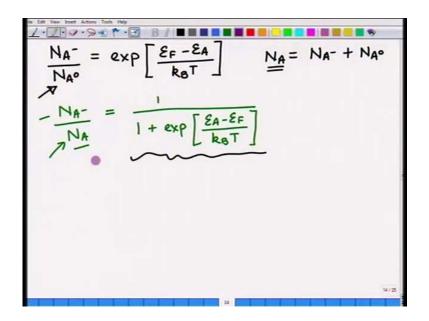
So, this is where we were a in the last lecture, what we have done was that we have derived this expression for ionization of dopant. So, if you have a dopant and I am un considering now a monovalent dopant, namely that which has only one ionization; that means, it can go from 0 charge state to plus 1 charge state, a donor rather I should do pant I should say donor here, the d stands for Donor.

So, we have a donor which is monovalent meaning, that it can go from charge state to from 0 to plus 1 like phosphorous in silicon, where with it pie electron phosphorous is 0 when it gives away one electron, then it becomes phosphorous plus; that means, d plus. So, if, so then if I add, so much quantity of 10 to power 16 per centimeter cube, of phosphorous and silicon. Then question we asked is out of these 10 to power 16 atoms

that we have added of phosphorous in silicon, how many of them are in how many of what fraction of this 10 to power 16 goes into plus 1 charge state.

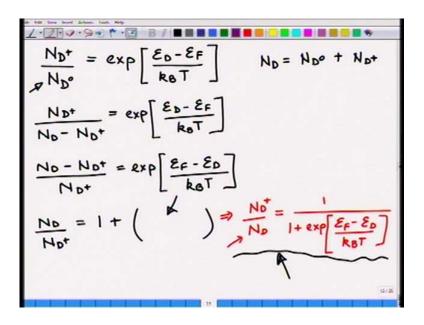
Of course, the remaining is in then there for 0 charge state. So, that how much what fraction goes into plus 1 charge state, is given by this expression in red and similarly, if were we have a acceptor, monovalent acceptor meaning there by something which is a 0 charge state normally, when it take and then it has it can also go in minus 1 charge state by accepting a electron.

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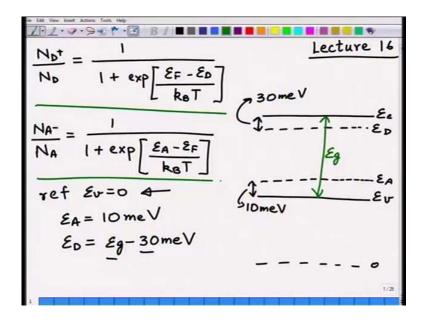
So, we have acceptor like boron for example, if I have added 10 to power 16 boron meaning this number here N A, if I have added 10 to power 16 number of boron atoms then the question we ask is, how many of them would go into minus 1 charge state. And that for all what what that fraction will be that fraction, will is given by this expression in which is written out in green alright. If, so then remember now, all these quantities are depending on where the Fermi energy is remember e A level is known, e D level is known.

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So, only thing n N A is known this is what you have added, we have added and N D is known this is what we have added. So, we can determine this N D plus and N A minus, if we know where the Fermi energy is, where this Fermi energy is that is a question we will answer little bit later. But, this is expression which tells us how much of it will ionize, at this point of time may be let us move on now, to our to our lecture now.

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first question let me just try to write this down again one more time for you this n N D plus over N D we are deriving as, 1 by 1 plus e to power e F minus e D by k t k B T. And

similarly we have deriving N A minus over N A has been equal to 1 over 1 plus e to power e A minus e F by Boltzmann constant times temperature. One thing which I did not mentioned in last lecture and something, which may confuse you sometime about what what these energies are. So, first let me point that out to you, that if you have this as a e c level this as a e A level somewhere is the 0.

Let us say here is the 0 energy. So, this reference to this 0 energy is this e A and e c level, so sorry e A e c and e v level valance band energy level. And now, if I add the do pants, then as I have already mentioned that do pant, donor is the this is the e D level and this is then there for e A level, as I have written there is no confusion, everything is measured with respect to this reference energy.

So, e A is this level which we are talking about; that means, it is measured from this origin here, from this reference here for a reference here. However, often if you look at a text book or if you look at a hand book, for this data, if you look at phosphorous and ask for where is the do pant level, then often it is said at 30 mille electron volts. For example, what they are then coating is that this energy difference, in that case the energy difference being coated is not really the e D level, with respect to some 0 because, at 0 is not specified.

If if a book gives you or a text book or a hand book gives you a number, if 30 mille electron volt for e D for example, then; obviously, it is assuming some reference. And you must know what what reference energy it is using, when they say some number like 30 mille electron for a do do pant then it is always with respect to the relevant band edge. In case of donor since since, it exchanges it is electron in conduction band, then this reference energy really is, with respect to the conduction band energy.

That means, 30 mille electron volts is this energy, then in that case this energy which I have just marked out here. If it and it similarly for boron, in silicon if a energy is given as 10 or 20 or 30 mille electron volts, as e A level acceptor energy level, then what is being coated is, this energy difference with it is with respect to the band edge. So, there for when you when you work on this, then you a need to keep track of it then when you substitute this e D and e A in here, you cans substitute this as 30 mille electron volts.

You have to substitute it, with respect to a common reference common reference which is somewhere somewhere we do not know where that is is, choice it does not matter because, you always dealing with difference of energies. So, whatever is the reference that will cancel out, the only point is whatever is the reference, you choose for e D here same reference you must choose for e A also and then whatever the Fermi energy you will deal with, must have also the same energy level, as long as you doing that then you are.

So, for example, if this was 10 mille electron volts, mille electron volts and this was let us say 30 mille electron volts then what then I will first choose a reference. Suppose, I choose a reference as e v equal to 0, suppose I choose my reference energy to be right here itself, e v to be 0 in that case I will choose e A then I will say is equal to 10 mille electron volts, is what I will choose and somebody ask me what is e D, then in that case e D will be, if this is whole thing e g, if this whole thing is band gap e g.

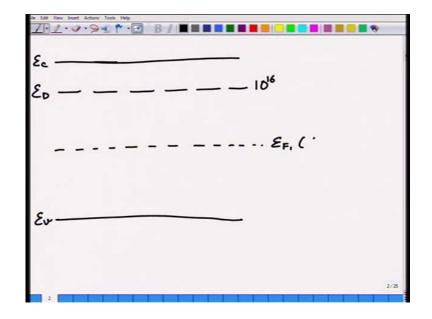
For example, in silicon 1.12, then I will write it as, I will write e D as e g minus 30 mille electron volts, of course e g also has to be taken in appropriate units or 30 has to be converted into electron volts also, you have to keep sure make sure the units are same, but you get the idea. That e D in that case is not 30 mille electron volts, but band gap minus 30 mille electron volts.

And when you then, use Fermi energy or you calculate Fermi energy, then that of course will also be calculated with respect to, whatever comes out, number comes out that always has to then in that case measured from, whatever you reference was which is e v equal to 0. So, these are point we need to keep keep in mind because, you will find often the numbers coated, in hand books which are with respect to the band edges respective band edges and you must, if you are going to use them for calculations, you must correct the you must make sure that you are using at least a common a common reference.

It does not matter what reference you choose choose, you are welcome to choose anything as your energy reference, as long as it remains the same reference for in throughout the calculations for every quantity, energy quantity all the energy quantities. So, thus one point we need to keep in mind now, second thing is let us look at using these expressions, let us look at ionization and a qualitative idea of where the Fermi energy would be.

So, let us begin with this begin with this and and we last lecture we are finished at at the point where we talking about, complete ionization and what does that mean, let us try to

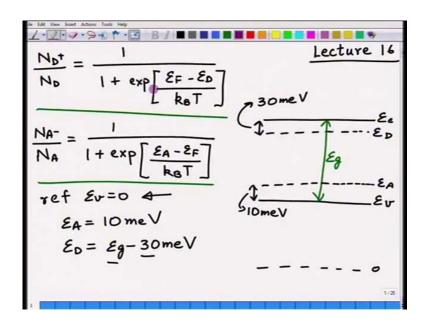
understand that for a minute. So, one thing I would like to point, out let us construct a picture again.



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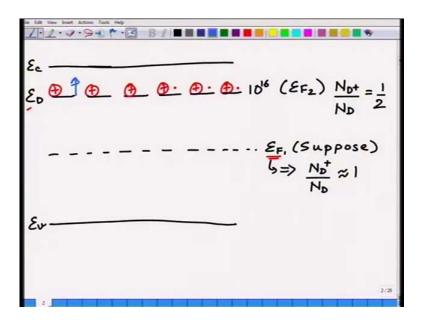
That here is my e c conduction bandage, here is my valance bandage and let us add some donors to it for example, let us just or add only donors no acceptors. So, if I add donors to it, like this here is the donor I have added added in it that is a 10 to power 16 of these or whatever that number is schematically speaking I have added here 1, 2, 3, 4, 5, 6, of them here alright. So, if, so then question I ask is can you determine qualitatively, let us look at how the ionization will proceed. Suppose, the Fermi energy was here, let us call it e F 1, suppose Fermi energy was here, this is suppose...

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Then let us look at this expression what happens. Once ask, what fraction of donors are the phosphorous atoms in silicon that I have added, how many of them are ionized what fraction is ionized. Now, notice e F in this case e F minus e D is what appears.

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Now, if you look at e F 1, so there for if you look at e F minus e D, in this picture that I have show I am showing here, this picture I am showing here, you see that this number would there will there will there for will be very large negative, will be very, very negative number. Because, e F is much, much smaller than e D, so it will be a large

negative number what is that mean. That means, that what; that means, is that this exponential quantity will be almost 0 this quantity will be almost 0.

And hence, what will we have this n ratio of N D implies, this implies N D plus by N D is almost nearly equal to 1 that is what this implies. That means all donors will be ionizing, that you can clearly see that if I if i have; that means, I will be left with all only plus t plus or donor plus atoms here, at this level I will show p plus the electron, the fifth electron which was with it is gone it is ionized. Why it this happen, you must also understand this Fermi energy, represents average energy approximately not exactly precise the, but ave it represents approximately average energy of the entire electron system.

If, so you can see the since Fermi energy is much, much smaller than e D there for, why should electrons continue to sit at e D level, there we can lower the energy by going to e F level. So, will we since e F they, so also rough roughly speaking something like that, that there for they will rather live at e F level, if that represents a average energy. So, there for, it is logical to think that all phosphorous atoms would be ionized and, as long as e D minus e F is substantial.

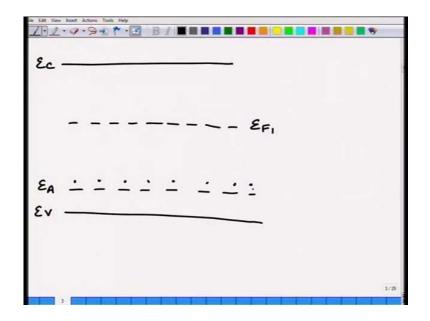
Let us say, more than three k t more than three k t in that case, what do will have is that all the phosphorous atoms will be completely ionized. Now, suppose Fermi energy is right here, this is if su if you suppose Fermi energy is moved right here, I am writing as e F 2 let it little bit more cleanly here. So, it is Fermi energy is moved here, just where the e D level is, if we move at up to e D level, then what happens in that case let us look at this expression again, let look at this ex expression then we get e F is equal to e D; that means, N D plus by N D this fraction is exactly half, this fraction is half.

So, if Fermi energy le leave riches the donor level then what; that means, is half of the phosphorous atoms, in silicon would have given their electron, away where as half of them would be half of them would have give given its electrons, half of them will be not ionized. That means, half of these would be carrying the electrons, with it e D level was right there, let us go back again to one more thing e F level just see it in this context, since even e f was very low then where does the fifth electron go.

We think that this fifth electron off's goes to the conduction band, but then you can ask that, why what do you mean by that. Well remember, this average energy of electron system this e F e is e F which suppose to represents, if suppose to represents all the free electrons all the free all the electrons. So, electrons in the valance band and also the electrons, in the an and the electrons, in the conduction band, it suppose to represent that. So, if there is electrons in conducts, so some though we say some electrons have gone in there, this average of all these energies is e F 1.

So, it is that electrons, in case of e F is le lower than e D, that they go into conduction band because, then they are there effect is being taking care of in the, average energy calculation. So, you can think like this, now similarly of course, if Fermi energy continues to move upwards, if a Fermi energy was still higher, than in that case this ionization would be, what will what will happen then, in that case e f will be much, much greater than e d when it, so happens.

In that case, this number can become very large and then what will have is N D N D plus by N D would then start going towards 0. In that case no atom will be of phosphorous in silicon, no donor atom would be ionized; that means, all of them neutral their fifth electron will be tied up to, the do pant itself. So, thus the picture you can look at how by moving Fermi energy the ionization occurs.



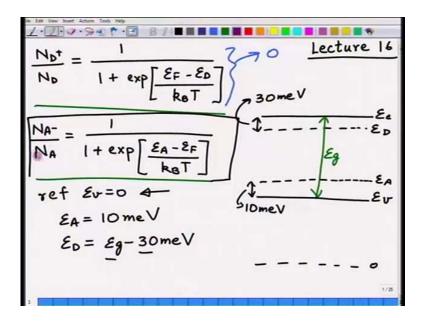
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Exactly, same argument applies for val for acceptors also what I mean is here acceptor levels, this is e c, this is e v and this is e A if, so then you can clearly see, if Fermi energy was high up here. Then what will happen e F 1, if energy was higher up here, then what

will happen without looking at the without looking at the mathematical expression qualitatively you can see that, if Fermi energy was higher, that is the average energy of the electrons and e A level is lower.

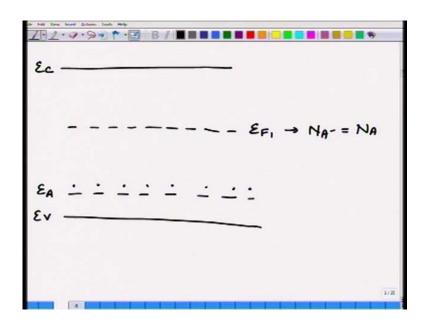
Than might as well these electrons, why would they are not occupied, they would then occupy lower energy state would be that they occupy the acceptor level. So, in that case if electrons are going to be found if Fermi energy is here, then we should be able to find electrons over at on e A sight. So, there for; that means, acceptor are ionized, so these acceptors are boron; that means, they are not they are in boron minus state, they have accepted the electron.

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And that you can now see from this mathematical expression also, if e F is much, much greater than e A, then this difference is a large negative number and hence this is approximately 0 and then N A minus by N A.

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In that case N A minus is equal to N A; that means, complete ionization, all the boron atoms are completely ionized. You can proceed this same argument, when e F Fermi energy is exactly at e A, then half the boron atom should be ionized and half would be neutral. And similarly, if you go further down in fa further down in Fermi energy, below e A level, in that case then no boron atom will be ionized, they all will be neutral in that case.

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ompensation OK

So, that is the big picture which you can look at, second aspect before I move on, I will talk about compensation. Now, what I am going to do is, I am going to add two types of two types of both a two types of do pants, in a semiconductor e c, e v back to the same picture and now, let me add this time both acceptors and donors simultaneously. And I am showing fever of, fever of acceptors levels and more of donor levels similarly, I could draw a something like this, more of acceptors and fever of donors.

So, what would happen in this case let us in this case what will happen, suppose number of donors I have added is N D that is the number of donors I have added, per centimeter cube and N A is number of acceptors that I have added per centimeter cube. So, I ask the question, that let us say temperature is 0 k for temperature is let say 0 k what is the picture at 0 k. Now, notice what I am going to do is here is the phosphorous atom with its electron fifth electron here is phosphorous atom with its fifth electron.

Now, what happens to rest. So, let me draw the ion for even at 0 k the fifth electron, you see at least four of those in this schematic picture could have gone down here, they can system can lower its energy, but sending the electron four electrons down here, which were which I could have put here, here, here and here. So, even at 0 k the ground state would be that I would have, two atoms of phosphorus in this schematic, in this schematic two items of if use the different colors or hairs electron my electron here, electrons are right here.

So, here is the four electron this electrons. So, four of them have ionized, four of these electrons has gone do gone down to boron side that way system can lower it is energy, total energy of electron system can be lower, even at 0 k. So, if I now raise the temperature how many electrons can go to the conduction band, only these two only these two can go to conduction band, when I raise the temperature. If, so there for what do you expect, the electron number of electrons to be even at 0, first of all how many how many a how many do pants how many do pants N D will be not ionized that clearly number is equal to N D minus N A.

That means, even at 0 then; that means, if I a a this number is 10 to power 16 and this number is 10 to power 15 is what I added center of 4 fif 15 boron in silicon and to a 16 phosphorous in silicon, then 10 to power minus 16 minus 10 to power 15 sorry 10 to power 16 minus 10 to power 15, is the only number of donor atoms, which can

participate rest of the rest are already compensated. And hence, such semiconductors are called compensated semiconductors.

That means this is the only n D minus n A is the concentration n which I can expect which in this case is two, this is the n I can expect here, when I raise the temperature that is the that is the only amount I can get. I hope you can note see this. qualitatively which I will prove a exactly precisely. Similarly by same arguments, you can see here in this case where I have this phosphorous. So, many phosphorous levels e D and which means n D of these I have added here.

Now, in this case this is, this number is 10 to power 15 and this number is 10 to power 16 what will happen, these electrons will at least these four electrons will come down here even at 0 case, suppose temperature is 0 k. So, this four electron will come down here, so what do we see, we see that in this picture just like in this picture, all n A were ionized and n A number of donor same number, of donors have also ionized at 0 k.

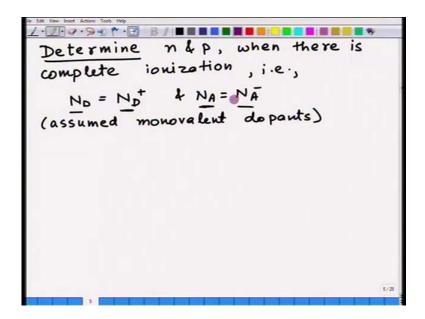
N D minus N A there for were not ionized, these are the at 0 k when I raise the temperature it is only these which can give there electrons to conduction band there for same argument here, in this case these electrons would have come down here, even at 0 k. So, there for now say N D smaller than N A in this case all N D atoms will be ionized, whatever is the number of N D that many number of acceptors will also be ionized boron minus.

So, the once which are not ionized are there for N A minus N D, this is the number which is not ionized right here, these are the one which are not ionized they did not have taken the electron, they have not accepted the electron. So, boron is still in 0 charge state, when they accept the electron only then they will go to minus 1 charge state; that means, when I raise the temperatures, temperature the once which will accept their electrons and create hole will be equal to only this.

So, this is the number which can then, create the holes when I raise the temperature. So, these are the few things these are few things which qualitatively you can understand, now having given then explain explanation let us look at that then, let us look at what happens let us look at a this compensation idea mathematically also. So, what is our picture, our picture is a is something like this, I know I know my n is equal to n c times e

to power e F minus e c by k T. Similarly, I know my p is equal to (no audio from: 23:32 to 23:42)

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Now, my problem is question is determine, so these are by assumptions, you want to determine n and p. When there is complete ionization, what is that mean; that means, when N D is equal to; that means, whatever do pants have added, whatever donor I have added, phosphorous atoms I have added all of them have ionized, whatever boron atoms I have a a a added in silicon, all of them have ionized. And we are assuming monovalent,

you are assuming monovalent implies that N D is equal to N D plus, plus N D in 0 charge state.

Similarly, we mean by monovalent do pants as N A is equal to N A minus plus N A in 0 charge state, they only two charge state phosphorous 0 and minus 1 charge state, that is what is implied by monovalent do pant, it gives only one transition and only for one charge state, to another charge state only exactly one transition is what it does. If, so then I am interested in doing what n and p's, that n and p off-course is given by this expressions, which is just written here these are general expressions.

So, only thing were we know need to know is, where is the Fermi energy if we knew where the Fermi energy is, then we can solve this problem, just like an intrinsic case, in intrinsic case what did we do. In intrinsic case, what we did was that we wrote down the definition of intrinsic condition, that n is equal to p when, we wrote n is equal to p that expression then gave us, where the Fermi energy is we calculated e I, since that the Fermi energy level.

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Something similar will have to do here, will have to define now what additional we need one more additional condition alright. So, let us write down that additional condition, that additional condition if you look at in intrinsic semiconductor n equal to p was very natural for us to write, that exactly represented charge neutrality condition. That means, that you see when a electron is created, by thermal means in a intrinsic semiconductor, correspondingly I must have positive charge also, if I did not then charge neutrality would not be maintained.

So, in to maintain the semi in a semiconductor pure silicon, then I there for demanded that n should be equal to p, that we did wrote in a logical way, but if you think about it that really represents charge neutrality. In this semiconductor also, where we have doped the charge neutrality may be should be maintained; that means, in silicon the boron I add the boron atoms are not charged, they come with three electrons; that means, they are in 0 charge state, that is what I have added in silicon I have added phosphorous, they come with five electrons; they are in not charged they are not charged.

They are neutral that is what I have added in here, but when they go in there, some of them depending on where the Fermi energy is some of them will get ionize and some will not be ionized, when they ionize they have a charge. If they have a charge, but the net total charge must say will be 0 that is what the requirement on this semiconductor will be. So, let us write down this ex charge neutrality condition.

It is a what are the negative charges, my negative charges are n what else is my negative charge of course n A minus, the boron atoms I have added and they have become minus charge. Some may be 0 charge state, but since I am assuming complete ionization in this case of course, there for I will assume the immediately I will assume that same thing, all of them are ionized just after this. But, this is general expression similarly, number of positive charge should be equal to positive charge and positive charge is because, of holes plus N D plus, this by charge neutrality condition.

Now, let us look at the consequence of these assumption, I have made assumption that N D is equal to N D plus N A is equal to N A minus as a consequence of complete ionization what; that means, is that if this is e c, this is e v I do not need the details of where e A and e D levels are e D and e D as long as Fermi energy is somewhere, in this region here below e D and above e A then I have shown to you, that complete ionization will occur. As long as e A is below e above e A Fermi energy is above e A and below e D, then all the do pants will be ionized.

Let us say 3 k T from each of these points, these are a Fermi energy away from that, in that case that all the do pants will be ionized. So, since I have assumed that there is there

is complete ionization, I am assuming that Fermi energy is going to whatever Fermi energy we calculate, is going to come out in between these region.

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And if it is going to come out in this region, then clearly then, these expressions also for n and p are also valid in this region. In this region as you know 3 k T away from e c and e v these expressions for valid and hence, these expressions would be still be valid if Fermi energy is even 3 k T away from e D and e A levels. So, there for in that case complete ionization will also occur and these expressions will be valid which I am about to use there for.

So, first thing I will write in this special case, while this is a general case and the special case there for I will write, in this special case n as equal to N plus N A equal to p plus N D. So, I will write it down like this because, N A minus was equal to N A and N D plus was equal to N D now let us, solve for a case if N D is greater than N A let us assume, u called assume N A is greater than N D that is find, will find what the result it gives us. So, let us continue on this next page.

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So, n plus N A is equal to p plus N D alright and we, assuming N D is greater than N A you do the exercise, when N A is greater than N D just like I am proceeding here. If, so then I have another expression remember since, n is equal to e to N c times N c times e to power e F minus e c by k T and p is equal to N v times e to power e v minus e F by k B T. And remember, product n p we have shown is equal to n i square, that we have that also we know, that must always be true.

So, I am going to write this expression as n plus N A equal to n i square by n plus N D you see now since n i square I know,, since I already know about n i square is it is a material proper property recall n i square is equal to N c N v e to power minus e g which is band gap by k B T that is what we had calculated. So, there for I know what n i is

since, I know the band gap whatever temperature you are talking about, this is a constant, this is a material was the material specified is the density of states of that material.

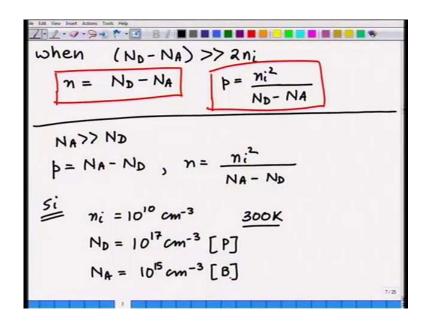
These are numbers, which unknown and the expression for that was also given, if you know the effective mass then you know N c and N v alright. So, if that is the case then since I know n i there for I can substitute in here, now you see is this expression only in n and I can easily calculate, what the value of n will be in this case. So, this is that is half simple it is, so let us do that, so I am going write this as n square minus N D minus N A times n and minus n i square as equal to 0.

So, I have a quadratic equation here, so what is then equal to n is equal to n is equal to N D minus N A plus or minus square root of 4 divided by 2. So, that is what n is equal to clearly you can see, that I must drop this minus sign, I must remove this minus sign from here plus. Because, this quantity will be this, quantity in square root will be greater than this quantity and since n is a positive number.

So, I there is no possibility of having a negative sign, so I am going to remove that now. So, I am going to remove this this sign and a only sign I can carry is plus, so that n is equal to, once I know n I can go back and determine, what e F is I can substitute in here, what a a and of course p is equal to then n i square by n. So, you can determine n and p and of course, you can determine by where the Fermi energy is from either of these to expression, will give you same number does not matter, that will be that come out always right. Because, n i square is this quantity anyways it will be used, a these two expression is being used there. Let us, but proceed further little bit.

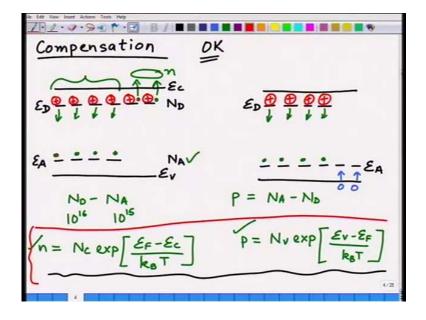
When much, much greater than not na greater than equal to 2 n i is greater than 2 n i, in that case what happens I can drop this term this 4 n i I can drop this term because, N D minus N A square will be much, much greater than 4 n i square in that case. If, so then what will happen, then I will get basically n is equal to N D minus N A is a key result. And; that means, also that p is equal to n i square by N D minus N A alright.

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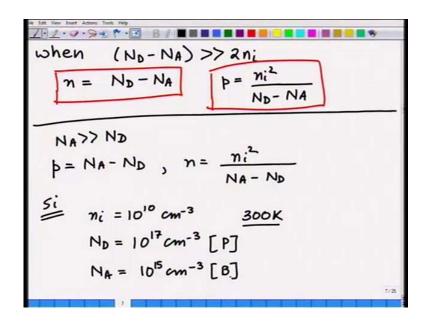
If you were to if you were to do the same exercise, which I am asking you a which I have asked you to do that N A is greater than N D, then you solve for p in that case, in that case you solve for p and you will find by going through exactly same process process, you will find p is equal to N A minus N D. And you will find there for n is equal to n i square by N A minus N D, since N A is much, much greater than N D. So, these are the two values will get and you can determine, where the Fermi energy is also based on any of these expression.

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Now, notice this result which we got, this n is equal to N D minus N A recall, that this is exactly what the compensate semiconductor was when we said. Since, in this case donors are greater than N A. You will recall this picture of this picture we have drawn here, where N D is greater than N A I have shown six levels here and four levels here, if that happens even at 0 k we had said that at least four of these whatever is the number of N A would have, would be ionized already and these equal number of donor atoms would also be ionized.

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What is left at 0 k is N D minus N A which are not ionized, which eventually will give you the electrons, in the conduction band when you raise the temperature and that can then be in N D minus N A, notice, you got an that results now. We, have gotten that results that n is equal N D minus N A and of course conversely if N A was greater than N D, then precisely similarly, you are going to get the exactly same way you are going to get N A minus N D.

May be is worth looking at some numbers, in silicon for example, n i as you know is about on the order of at room temperature, at 300 k that is I am talking about 3 and k 10 to power 10 per centimeter cube. Now, suppose you have N D which was equal to, let say this we go to 10 to power 17 per centimeter cube this, is the phosphorous atoms you have added for example, this many and N A is equal to let us say 10 to power 15 per centimeter cube, this many boron atoms you added.

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1017 - 1015 ~ 1017 cm  $= 10^{3} cm$ 

Then what is n equal to n clearly there for is approximately equal to 10 to power 17 minus 10 to power 15, which is approximately equal to 10 to power 17. That's what the value of n will be, then what will be the value of p value of p will be there for equal to 10 to power n i square 20 divided by 10 to power 17 which is equal to 10 to power 3, here is this number let me write down this more cleanly here, 10 to power 3. Now, this requires little bit of discussion.

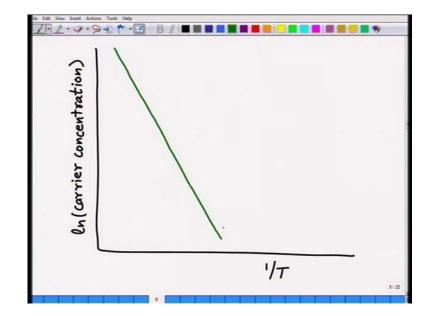
Now, I am going to put this n red here caution, this is the matter which confuses some students and often I get this question. That calculation we have done shows n is equal to 10 to power 17, this students often I believing to accept because, this you see that I have added 10 to power 7 for 17 phosphorous atoms. So, since they can give the electron to conduction band.

So, one can ace accept the fact that there are 10 to power 17 number electrons, in the conduction band. What call this problem is this concentration of holes, why the argument made is as follows that when you have a intrinsic semiconductor, since we are saying n i is 10 to power 10; that means, at least 10 to power 10 electrons are here, and 10 to power 10 holes are here. That is what it means, intrinsic electron concentration that if the semiconductor were intrinsic it was not doped, in that case I would have gone an 10 to power 10 electrons here and 10 to power 10 holes here, this p and this n would have been this number.

Then I have taken this semiconductor at same temperature 300 k and added now, some phosphorous and boron does not matter boron is there or not because, any way all boron is compensated right, upfront anyways it is like 10 to power 17 and if you added 10 to power 15 both 15 boron, then 10 to power 17 10 to power 15 of the phosphorous atoms are anyway, gone to compensate boron. Once they, have compensated then that is out of our calculations, only part, only thing which we going to play part is what is remaining phosphorous phosphorous atoms.

So, anyways, so whether you added only phosphorous atom or you added boron that is not the issue, boron also that is not the issue let, so but, point is that if you added do pant it is a do pant dominated. Then in intrinsic semiconductor itself you have 10 to power 10, electrons in 10 to power holes when you added phos do donor atoms then n is increase 10 to power 17, that is acceptable that y z that holes have become, only 10 to power 3 even in intrinsic case, they were 10 to power 10 why did they became less, at least they should have been 10 to power 10, that is one problem which I often here from students.

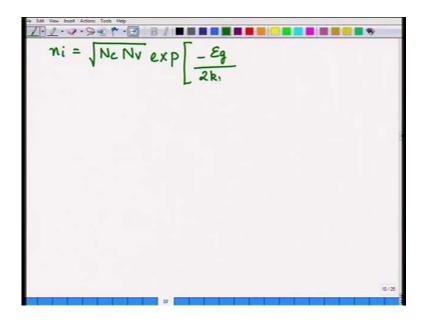
So, I would like to clarify that point to you in a minute. Then us this I will try to clarify to you through a plot, and then hopefully a keep this question I mind, this I am posing as a question which may be in your mind also and I will try to clarify it, but I will do this do, so through an plot, through a plot.



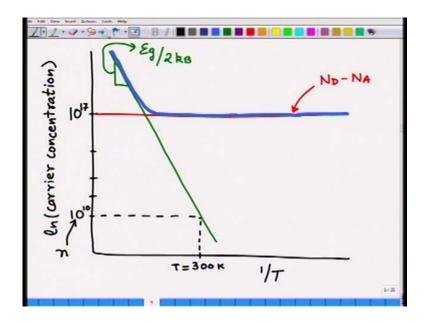
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So, let me re plot something which was talked to you earlier, if I plot 1 by T verses log of, so is less space here carrier concentration. Whether n or p does not matter n or p whatever, whichever way is more dominant, whichever is more dominant is what we are plotting. You will recall, the way we have plotted was that here is a semiconductor silicon or whatever it is we had said that intrinsic electron concentration.

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This n i varies as N c N v which has a very week temperature dependence 3 to power 2 a type of temp temperature dependence e to power minus e g bad gap by 2 k B T that is

what we had written. And there for we had said that the slope would be equal to e g correspond to e g by 2 k B that is what we had said. Alright now let us say 3 unit k, this is silicon if you looking at 300 k let us say, then what happens. At 300 k here is let us write down a number here somewhere, let us mark out the number somewhere here is 10 to power 10 let us say, in case of 10 to power 10 which means I am plotting on log scale, but this is n, this is n equal to this much n i or n carry concentration is equal to 10 to power 10 percent limited cube.

And if you plog a log of natural log of it, then it is actually natural log of 10 to power 10 that is what this number represents here alright. So, if that is the case then right here and this temperature is 300 k, this temperature is corresponds to T corresponds to equal to this. So, I am plotting 1 by T of course on this scale, but the temperature here corresponds to at this point 300 k. So, this is 10 to power 10 is n I, in case of silicon which is what we have used here.

But, now notice that what happens to my n, my n is equal to N D minus N A and it is independent of temperature, it is independent of temperature it is about 10 to power 17 in this case, in this example which I have constructed here. So, let us plot this n here now also will separate line on the same graph, so if I do, so then let say let us say that here is a somewhere here is 10 to power 17, let us say and number being repress it is an log scale. So, this is 10 to power 11 10 to power 12 etcetera etcetera and, so on or maybe I take it even higher here.

If, so then let me plot this line here. So, this is line is right here, what is this line representing, this line is representing N D minus N A about 10 to power 17 in this case which have been added in our example, which we have taken. Now, what is in this semiconductor carrier concentration as a function of temperature, clearly that profile will look something like this, meaning there by, this is blue line basically if it the semiconductor was intrinsic, then it would have followed green line.

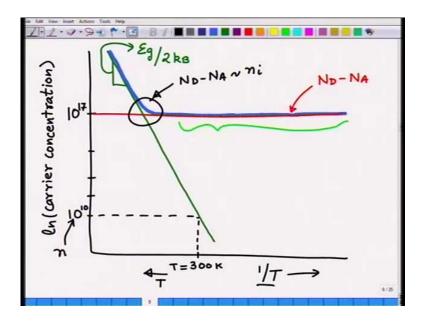
But, when semiconductor is when semiconductor is doped also then the concentration will follow blue line, why clearly now this is where you can clearly see that at 300 k what happened was I had N D minus N A as 10 to power 17.

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$n_i = \sqrt{N_c N_V} \exp\left[\frac{-\varepsilon_g}{-\varepsilon_g}\right]$	
1017	
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That means, that when I had this semiconductor right here, I had at 300 k 10 to power 17 electrons could go up into the conduction band, intrinsic concentration anyways very, very small it does not dominate.

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But, when we go to temperatures higher and higher this way is temperature is increasing, temperature increasing 1 by T what I am plotting in this direction. So, temperature is increasing in this direction, so either it go to higher, so I start it somewhere here and I go for higher and higher temperatures, as I go to higher temperatures. Remember around

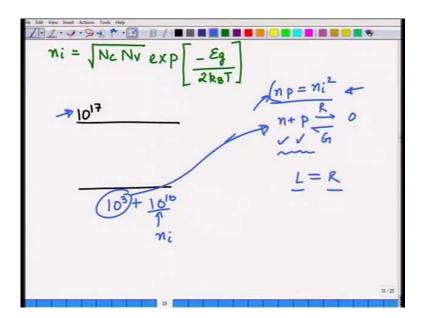
this area, what happens in this region your N D minus N A is now comparable to in this point, they equal N D minus equal to n i.

And what was my assumption here notice N D minus N A should be much, much greater than 2 n i or n i does not matter, two factor of when we talking about order of a magnitude it matters does not play serious part. So, N D minus N A should be much, much greater than n i. So, clearly that is true in this region, in this region it is true. In this region where n i is this green line and N D, N D minus N A is this red line and clearly red line is much, much great higher greater than this blue line remember this is log scale.

So, this much, much greater, but in this circ in circle region this two numbers start becoming equal and what happens, while this number will continue same way. But, now since temperature is large enough, the intrinsic carrier concentration itself is very large and there for carriers are generated through intrinsic means rather than dope dope semiconductor.

That means, a dope semiconductor if you dope it and you, but you continue to raise the temperature, at some point of time it starts behaving like an intrinsic semiconductor. So, that is the point that this is 10 to power 17, when 10 to power 17 and when temperature is large enough, then anyway when intrinsic co carrier concentration becomes 10 to power 17 or higher, then this number becomes even large and starts following green line.

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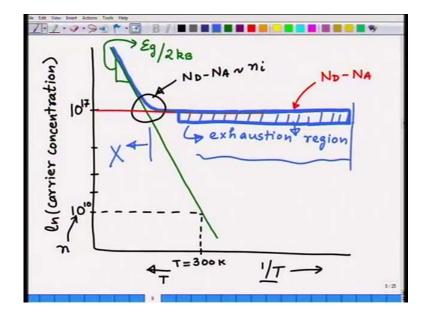
Because, of intrinsic region fine, but always remember this that an any given temperature even at 300 k, when 10 to power 17 I do have a law of mass action It is; that means, that recall that n plus this came from n plus p recombination and generation and the generation process, this equilibrium between these two processes is dictated this, that n product n and p should be con constant. If you; that means, increase this then you must decrease this.

You cannot there for argue that even at 300 k when p was when n became 10 to power 17 for the reason shown, they cause it is doped semiconductor. So, an n became 10 to power 17 I did not 300 k, although intrinsic semi, intrinsic means could have given 10 to if it semiconductor intrinsic, then p would have been 10 to power 10 the holes would have been 10 to power 10. But, know they cannot be because, the in thermal equilibrium this condition should always be maintain that n p is equal to n I, square that condition is requirement of thermal equilibrium.

Just in the same way, as you have studied in your chemistry course, where if you take two reactants and if you increase one, if you increase one then other one concentration of other must be decrease, to keep the rate of reaction the same forward in an cu maintain the equilibrium. You to maintain a equilibrium either whatever is the on the left hand side of the equation and right hand side equation, chemical equilibrium either if one quantity on left hand side goes down then other must increase or proportionately on the right hand side everything must increase correspondingly.

Since, in this case it is annihilation of electron and hole that cannot happen, there for only option left is if one increases other must decrease. And that is the reason it requirement of thermal equilibrium that is leading to that thermal equilibrium is leading to that p must become 10 to power 3, you must never think of concentration in valence band as being equal to this 10 to power 3 plus 10 to power 10. Because, that is coming from n i means, this is a common mistake, which students make that they think that n i, n i is because, of this reason it because, of this reason.

And because, of this n i anyways n i was 10 to power 10 at at room temperature, there for add that that number also to it, that will be wrong. This is the hole concentration in valence band because, you are required to maintain this particular equilibrium because, this particular equilibrium, must be maintain and hence you must have product n p equal to constant and hence this must become 10 to power 3.



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So, that is the point now, this is what is called exhaustion region. This is where, really the semiconductor semiconductors work. So, this is clearly or or this is or maybe I can go further may be erase this one, draw a little nicely now this is the region this is the region, this is the exhaustion region for a semiconductor. A semiconductor would normally we used in electronics in this region, so I will finish my class here, and I will show eventually you understand the point that whether semiconductor is doped or un doped.

If temperatures increases beyond this point if it goes temperature on this side, then semiconductor will behave like intrinsic semiconductor. You also know devises are made when you have a N junction and a P junction, you make a P N N P junction, but in temperature range is on this side, since semiconductor starts behaving like an intrinsic semiconductor. So, it does not matter that is N type or P type do pants play no role, so where if you try to make a junction and heat it up too much, it go to high temperatures.

Then n characteristic and p characteristic are lost and there for junction is lost all you have is a semiconductor, intrinsic semiconductor. And see hence devise will stop operating so; obviously, you cannot operate in the intrinsic range, semiconductors are made n and p type that you can clearly do, only in the exhaustion region.

So, I will stop my lecture here, next le lecture I will show I will talk you about fees out carrier, fees out characteristic and that will be last lecture on this a equilibrium carrier concentrations. And in that case I will show you, how temp what happens if you go low, so I am talk I have talked about high temperatures what will happen, from this exhaustion region, if you go to still lower temperature what will happen that I will speak about, I will talk about in the next lecture.

Thank you.