M.Sc. DEGREE EXAMINATION,NOVEMBER 2018
II Year IV Semester
Core Elective - IV
MICROPROCESSOR AND MICRO CONTROLLER

Time : 3 Hours **Max.marks :75**

Section A $(10 \times 2 = 20)$ Marks

Answer any **TEN** questions

1. Give the main function of DMA controller
2. What is called handshake input?
3. What is pipe line processing?
4. What is a stack segment register?
5. Mention the use of NMI
6. Define based addressing mode
7. What is LDS instruction?
8. What you mean by flag control instruction?
9. What are special function registers of 8051
10. Give the comment of the mnemonics ANL A,Rn and ANL direct,A
11. What is memory bus cycle?
12. Define memory mapped I/O

Section B $(5 \times 5 = 25)$ Marks

Answer any **FIVE** questions

13. Explain the internal architecture of the interval timer 8254
14. Describe the interfacing of Input fort with external memory
15. Explain the interrupt response of 8086
16. Explain the Min Mode of 8086
17. Discuss the compare and jump instructions with example
18. Write the assembly language program to add two eight bit numbers in 8051
19. Explain the interfacing of Keyboard using 8051 port

Section C $(3 \times 10 = 30)$ Marks

Answer any **THREE** questions

20. Draw the block diagram and explain the working of programmable peripheral device
21. Discuss the various interrupts in o 8086 processor
22. a) Describe data transfer instructions of 8086
    b) Write an assembly language programme for descending order
23. Describe the internal RAM and registers of 8051 microcontroller
24. Explain the interface of seven segment LED display using 8051 and write the programme for the same

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