**M.Sc DEGREE EXAMINATION,APRIL 2019**
**I Year I Semester**
**Computer Architecture**

**Time : 3 Hours**                                          **Max.marks :75**

**Section A** $(10 \times 2 = 20)$ Marks

Answer any **TEN** questions

1. Define Micro Operation and list out its types.
2. What are error detection codes?
3. Name the various computer instructions with example.
4. What are Flynn's classifications of computers?
5. What is an Array Multiplier?
6. Define Complements.
7. Define Strobe control.
8. What is Daisy-Chaining Priority?
9. Define Multiprogramming.
10. What is Cache Memory?
11. What are Binary Codes?
12. Define Memory Interleaving.

**Section B** $(5 \times 5 = 25)$ Marks

Answer any **FIVE** questions

13. Explain Micro Instruction format.
14. Describe the different Instruction formats.
15. Discuss addition and subtraction in signed 2's complement format.
16. Differentiate Isolated I/O and Memory mapped I/O.
17. Write short notes on Auxiliary Memory.
18. Explain briefly any three Addressing Modes.
19. Describe Asynchronous Data Transfer.

**Section C** $(3 \times 10 = 30)$ Marks

Answer any **THREE** questions

20. Explain in detail the construction of arithmetic circuit.

21. Explain Arithmetic pipeline with example.

22. Draw the flowchart for decimal division and explain.

23. Write in detail about: (a). DMA (b). Priority Encoder.

24. Explain any Two Mapping procedures of Cache memory.

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