B.Sc. DEGREE EXAMINATION,NOVEMBER 2019
III Year V Semester
Microprocessor Architecture and Programming

Time : 3 Hours   **Max.marks :60**

Section A $(10 \times 1 = 10)$ Marks

Answer any **TEN** questions

1. What is the different between nibble and Byte?
2. Convert $(19)_{10}$ in to binary number.
3. What are flag registers?
4. What is TRAP?
5. Distinguish between SUBB and CMP B.
6. Explain the function of RAR instruction.
7. What is the function of NOP and HLT instructions?
8. Write general form of move instructions.
9. What are subroutines?
10. Mention any two uses of stock pointer.
11. List out the INTERRUPT pins available in 8085.
12. What is a program counter?

Section B $(5 \times 4 = 20)$ Marks

Answer any **FIVE** questions

13. Distinguish static RAM with dynamic RAM.
14. Draw the pin out function diagram of 8085.
15. State logical instruction. Explain any two logical instructions.
16. Explain the different addressing modes of 8085 with examples.
17. Explain the Programmer's model of 8085.
18. Write an assembly language program for adding two 8 bit numbers with algorithm.
19. Explain the Demultiplexing of address / data lines.

Section C $(3 \times 10 = 30)$ Marks

Answer any **THREE** questions

20. Explain in detail the bus structure of 8085.

21. Define microprocessor, Draw the block diagram and explain the architecture of 8085.

22. Explain briefly the arithmetic Instructions with an example.

23. Explain the the Branching instructions of 8085 in detail.

24. Write an assembly language program for 8085 microprocessor to multiply two 8-bit Numbers.

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