

B.Sc. Degree Examinations - Even Semester 2021
III Year VI Semester
Integrated Electronics

Max Marks: 25

Answer any Five questions (5 * 5 = 25)

1. Construct the K-map for the three variable truth table in which the output is high for the following input conditions 1101,1010,0101,1111
2. What is the difference between a multiplexer and Demultiplexer? Explain IX4 Demultiplexer with the block diagram and truth table.
3. Draw the logical diagram of D-flip flop and explain its working.
4. Explain the principle and working Wien's bridge oscillator.
5. Explain with necessary logical diagrams the successive approximation A/D converter.
6. What is a half adder? Explain the function of half adder also discuss how it can be modified to function as a full adder
7. Explain the working of JK flip –flop. What is race around condition? How is it overcome? Explain these concepts with relevant diagrams