

B.Sc DEGREE EXAMINATION, EVEN SEMESTER 2021
III Year VI Semester
Microprocessor Interfacing and Applications

Max.marks :25

Answer any **FIVE** questions ($5 \times 5 = 25$) Marks

1. Draw and explain the timing diagram of opcode fetch machine cycle.
2. Describe the comparison of I/O mapped and memory mapped I/O interfacing.
3. Illustrate OUT instruction and its timing diagram
4. Classify the different kinds of interrupt used in 8085 microprocessor.
5. Explain the operation of 8255 PPI Port A programmed as input in Mode 1 with necessary handshaking signals
6. Draw and explain the timing diagram of I/O read cycle.
7. With neatly sketched the Programmable peripheral interface 8255