SHRIMATHI DEVKUNVAR NANALAL BHATT VAISHNAV COLLEGE FOR WOMEN (AUTONOMOUS)
(Affiliated to the University of Madras and Re-accredited with 'A+' Grade by NAAC)
Chromepet, Chennai — 600 044.
B.Sc. END SEMESTER EXAMINATIONS APRIL-2022
SEMESTER - VI
17UPHCE6001 **&** UPH/CE/6001 - **Microprocessor Interfacing and Applications**

Total Duration : 3 Hrs.   **Total Marks : 60**

Section A

Answer any **SIX** questions $(6 \times 5 = 30$ Marks)

1. Explain the timing diagram for Memory write cycle.

2. Prepare the design of an input port to 8085.

3. Describe the hardware interrupts. State and explain five interrupts in Intel 8085.

4. Explain in detail on flashing LEDs.

5. Explain the interfacing concept of 2k x 8 ROM.

6. Predict the generation of control signals for I/O interface.

7. Explain SIM instruction. How it can be used for 3 different functions?

8. Distinguish between Microprocessor and Microcontrollers.

Section B

Answer any **THREE** questions $(3 \times 10 = 30$ Marks)

9. Explain and draw the timing diagram for MVI Rd,data (8).

10. Explain the interfacing of 2K x 8 RAM with 8085 microprocessor.

11. Diagnose in detail about output port interfacing to 8085.

12. Predict the simple polled and interrupt controlled data transfer.

13. Organize the various blocks available in 8255 and its working.

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