SHRIMATHI DEVKUNVAR NANALAL BHATT VAISHNAV COLLEGE FOR WOMEN (AUTONOMOUS)
(Affiliated to the University of Madras and Re-accredited with 'A+' Grade by NAAC)
Chromepet, Chennai — 600 044.
BCA. END SEMESTER EXAMINATIONS APRIL-2022
SEMESTER - IV
08UCACT4006 **&** UCA/CT/4006 - **Computer Architecture**

Total Duration : 3 Hrs. **Total Marks : 60**

Section A

Answer any **SIX** questions $(6 \times 5 = 30$ Marks$)$

1. Convert 1100101110100102 to octal.

2. Write a note on Control memory.

3. Explain addressing modes briefly.

4. What is vector processing ? Explain.

5. Explain BCD adder with suitable example.

6. Write a brief note on direct memory access.

7. Explain Serial communication.

8. Explain address mapping using pages in virtual memory

Section B

Answer any **THREE** questions $(3 \times 10 = 30$ Marks$)$

9. Discuss about address sequencing in detail.

10. Explain RISC.

11. Discuss about Multiplication algorithm in detail.

12. Explain asynchronous data transfer in detail.

13. Write about interconnection structures elaborately.

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