SHRIMATHI DEVKUNVAR NANALAL BHATT VAISHNAV COLLEGE FOR WOMEN (AUTONOMOUS)
(Affiliated to the University of Madras and Re-accredited with 'A+' Grade by NAAC)
Chromepet, Chennai — 600 044.
BCA. END SEMESTER EXAMINATIONS NOVEMBER-2022
SEMESTER - III
20UCACT3005 - Computer Architecture

Total Duration : 2 Hrs 30 Mins. **Total Marks : 60**

Section A

Answer any **SIX** questions $(6 \times 5 = 30$ Marks)

1. Sketch decoders with truth table.
2. Explain Bus and memory transfer.
3. Compute address sequencing.
4. Explain instruction formats with example.
5. Describe Arithmetic pipeline with the flow diagram.
6. Compute array processors with example.
7. Classify the major difference between input and output interface.
8. Examine memory hierarchy with neat diagram.

Section B

Answer any **THREE** questions $(3 \times 10 = 30$ Marks)

9. Explain the arithmetic microprocessors.
10. Prepare the RISC pipeline with example.
11. Determine vector processor.
12. Solve the modes of transfer with flow diagram.
13. Evaluate the virtual memory.

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