

SHRIMATHI DEVKUNVAR NANALAL BHATT VAISHNAV COLLEGE FOR WOMEN
(AUTONOMOUS)

(Affiliated to the University of Madras and Re-accredited with 'A+' Grade by NAAC)

Chromepet, Chennai - 600 044.

B.Sc.Physics - END SEMESTER EXAMINATIONS - NOV'2024

SEMESTER -V

20UPHET5001 - Microprocessor 8085

Total Duration : 2 Hrs.30 Mins.

Total Marks : 60

Section B

Answer any **SIX** questions ($6 \times 5 = 30$ Marks)

1. Justify Microprocessor as CPU.
2. Discuss about memory read and write machine cycles.
3. Illustrate the data transfer operations with example.
4. How to write, assemble and execute a program. Illustrate with an example.
5. With a neat sketch explain 8085 bus structure.
6. Illustrate the hardware and software interrupts in 8085.
7. Write a note on one byte and two-byte instruction.
8. Write an assembly language program to arrange an array of data in ascending and descending order.

Section C

Answer any **THREE** questions ($3 \times 10 = 30$ Marks)

9. Explain 8085 Microprocessor Architecture with neat diagram.
10. Explain the interfacing input port and output port to 8085.
11. Explain the block diagram of 8255A Programmable Peripheral interface.
12. With suitable example, explain Logical and branching instructions of 8085 instruction set.
13. Write an assembly language program to add two hexadecimal numbers.
